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DATE MAILED: 08/04/2004

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/805,072	03/19/2004	Masahiro Maeda	10873.935USC1	7915
23552 7	590 08/04/2004			INER
MERCHANT & GOULD PC P.O. BOX 2903 MINNEAPOLIS, MN 55402-0903			GLENN, KIMBERLY E	
			ART UNIT	PAPER NUMBER
			2817	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/805,072	MAEDA ET AL.				
Office Action Summary	Examiner	Art Unit				
	Kimberly E Glenn	2817				
The MAILING DATE of this communication ap	_	orrespondence address				
Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a rep - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply be timely within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from e, cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).				
Status						
1)⊠ Responsive to communication(s) filed on 3/19	0/04.					
·— · ·	·					
, 						
closed in accordance with the practice under	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4) Claim(s) 1-22 is/are pending in the application.						
	4a) Of the above claim(s) is/are withdrawn from consideration.					
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-22</u> is/are rejected.	<u>, </u>					
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/o	or election requirement.					
Application Papers						
9) The specification is objected to by the Examiner.						
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
12)⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).						
a)⊠ All b)□ Some * c)□ None of:						
1. Certified copies of the priority documents have been received.						
2. Certified copies of the priority documents have been received in Application No. 10/142599.						
3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the International Burea	au (PCT Rule 17.2(a)).					
* See the attached detailed Office action for a list of the certified copies not received.						
Attachment(s)	.	(DTO 440)				
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 4) Interview Summary (PTO-413) Paper No(s)/Mail Date						
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date 3/19/04.	-	Patent Application (PTO-152)				

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DETAILED ACTION

Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claim 9 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Claim 9 disclose a first and second resistor in lines 24 and 25. Independent claim 7, which claim 9 depends, disclose a resistor in line 14. Is applicant reciting a total of three resistors or does one of the resistors recited in claim 9 represent the resistor disclosed in claim 7?

Double Patenting

The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

Claims 1-22 are rejected under the judicially created doctrine of obviousnesstype double patenting as being unpatentable over claims 1-13 of U.S. Patent No. Art Unit: 2817

6,741,144. Although the conflicting claims are not identical, they are not patentably distinct from each other because both the present application and patent each disclose 1. A high-frequency semiconductor device, comprising: an amplifier; a dielectric substrate provided on an input side or an output side of the amplifier; a plurality of transmission lines formed on a surface of the dielectric substrate and connected electrically to the amplifier; and a resistor formed on the surface of the dielectric substrate and 10 connected electrically between the plurality of transmission lines.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1,4,7,12 and 14 are rejected under 35 U.S.C. 102(b) as being anticipated by Seino et al US Patent 4,639,694.

Seino et al disclose in figure 1 a power distribution circuit comprising a dielectric substrate 1, distributed constant lines 2-7, resistances 8- 10, connecting lines 11-12, an input terminal 16, and output terminals 17-20. The distributed constant lines 2-7 and the connecting lines 11, 12 are formed of as microstrip on the dielectric substrate 1, and the resistances 8-10 are also formed on the dielectric substrate 1. The electrical length of each of the distributed constant lines 2-7 is selected to be a quarter wavelength. Each output is connected to an amplifier (not shown). See figure 1 and column 1 lines 7-55.

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Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 2, 5, 8, 13 and 15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Seino et al US Patent 4,639,694 in view of Sjogren US Patent 6,054,907.

The primary reference, Seino et al disclose 1 a power distribution circuit comprising a dielectric substrate 1, distributed constant lines 2-7, resistances 8- 10, connecting lines 11-12, an input terminal 16, and output terminals 17-20. The distributed constant lines 2-7 and the connecting lines 11, 12 are formed of as microstrip on the dielectric substrate 1, and the resistances 8-10 are also formed on the dielectric substrate 1. The electrical length of each of the distributed constant lines 2-7 is selected to be a quarter wavelength. Each output is connected to an amplifier (not shown).

Thus Seino et al is shown to teach all the limitation of the claims with the exception of the resistors having the same length as that of the plurality of transmission line.

Sjogren teach that resistance can be implemented as a quarter wavelength stub.

Column 6 lines 16-18.

One of ordinary skill in the art would have found it obvious to replace the general resistances of Seino et al with the art recognized equivalent quarter wavelength line as taught by Sjogren. The motivation for this modification would have been to provide an art recognized equivalent substitution.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure:

- Macheel et al US Patent 6,529,081,
- White US Patent 3,593,174,
- Hey-Shipton et al US Patent 5,616,539,
- Robert US Patent 4,875,024,
- Schnetzer US Patent 4,721,929.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kimberly E Glenn whose telephone number is (571)-272-1761. The examiner can normally be reached on Monday-Friday 7:30 to 4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (571)-272-1769. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Kimberly E Glenn Examiner Art Unit 2817

keg

Rober Pascal

Masima Johnson